

Figure 1

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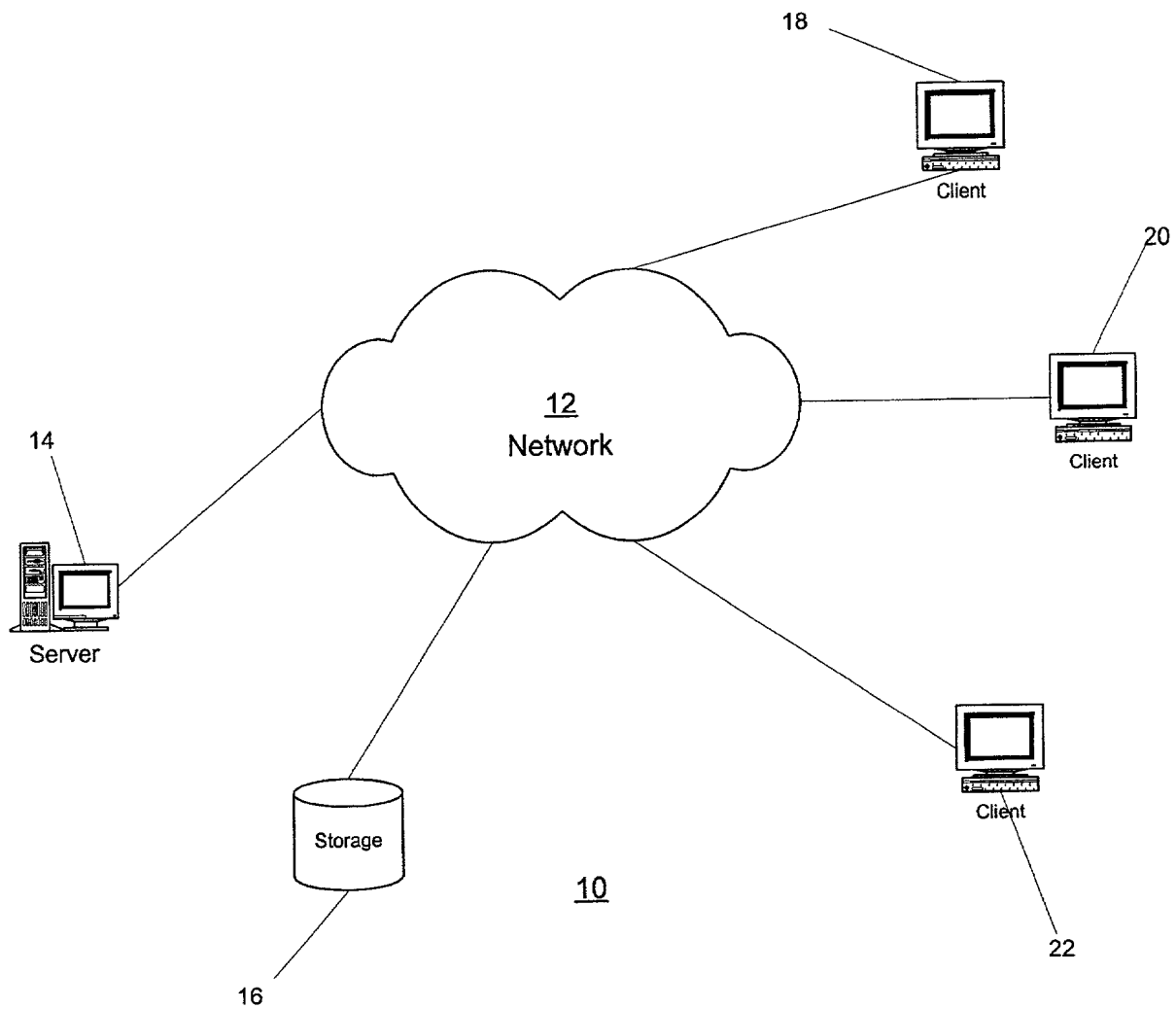


Figure 2 is a block diagram of a Data Processing System 100. The system includes four processors (101, 102, 103, 104) connected to a System Bus 106. A Memory Controller/Cache 108 and I/O Bridge 110 are also connected to the System Bus. Local Memory units (160, 161, 162, 163) are connected to the Memory Controller/Cache. The I/O Bridge 110 connects to an I/O Bus 112. Four PCI Host Bridges (130, 114, 122, 140) are connected to the I/O Bus 112. Each PCI Host Bridge is connected to an EADS unit (132, 116, 124, 142) via a PCI Bus (131, 115, 123, 141). The EADS units are connected to various I/O Adapters (136, 120, 121, 128, 129, 148, 149) via PCI Buses (133, 118, 119, 126, 127, 144, 145). A Service Processor 135 is connected to the System Bus 106 via an ATTN Signal and to the I/O Bus 112 via a PCI Bus 195. The Service Processor 135 is also connected to NVRAM 192, OP Panel 190, and a PCI/ISA Bridge 193. The PCI/ISA Bridge 193 is connected to an ISA Bus 196. A Memory 191 is connected to the System Bus 106 via JTAG/I2C Busses 134. The entire system is labeled 100 Data Processing System.

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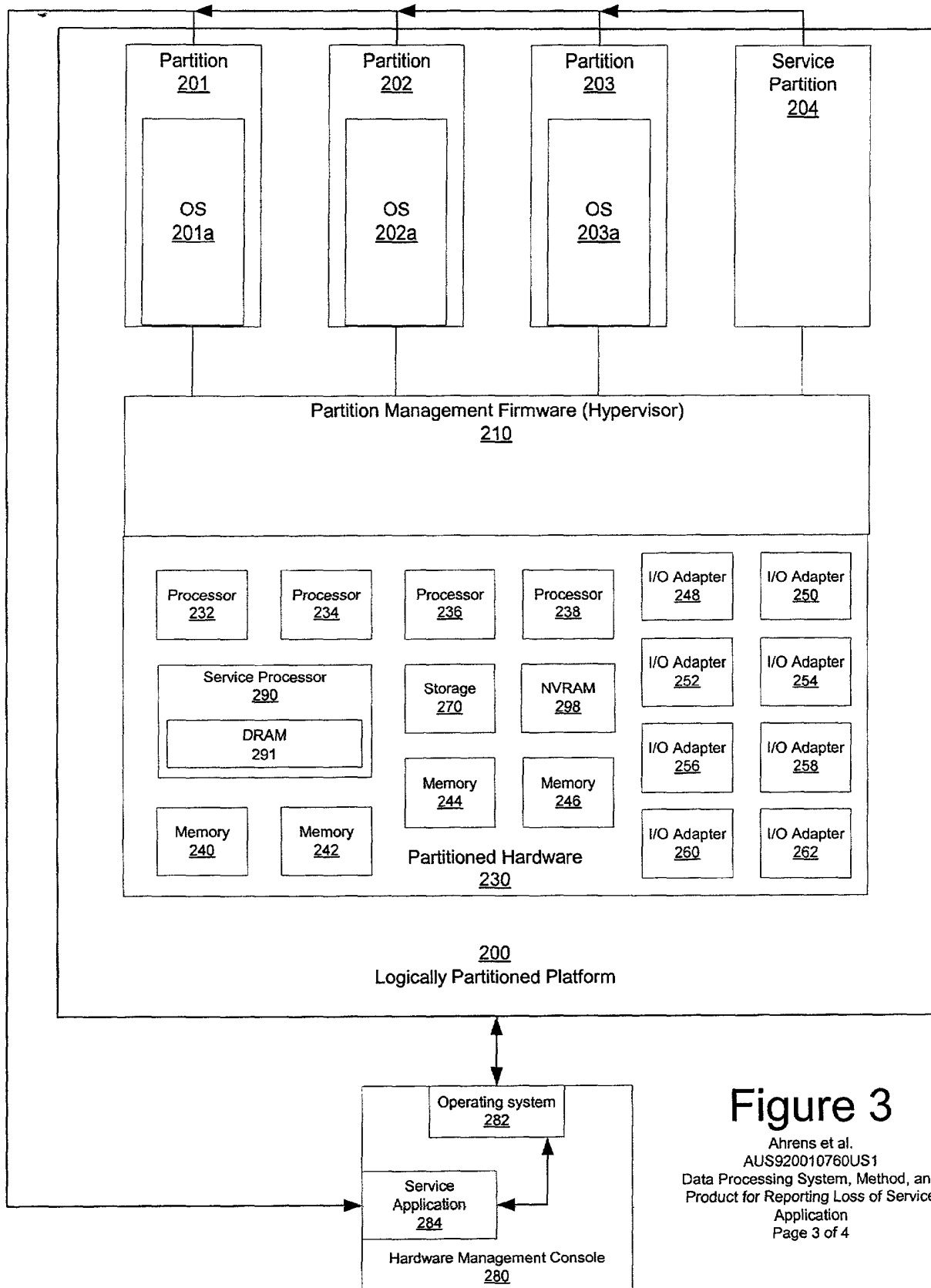


Figure 3

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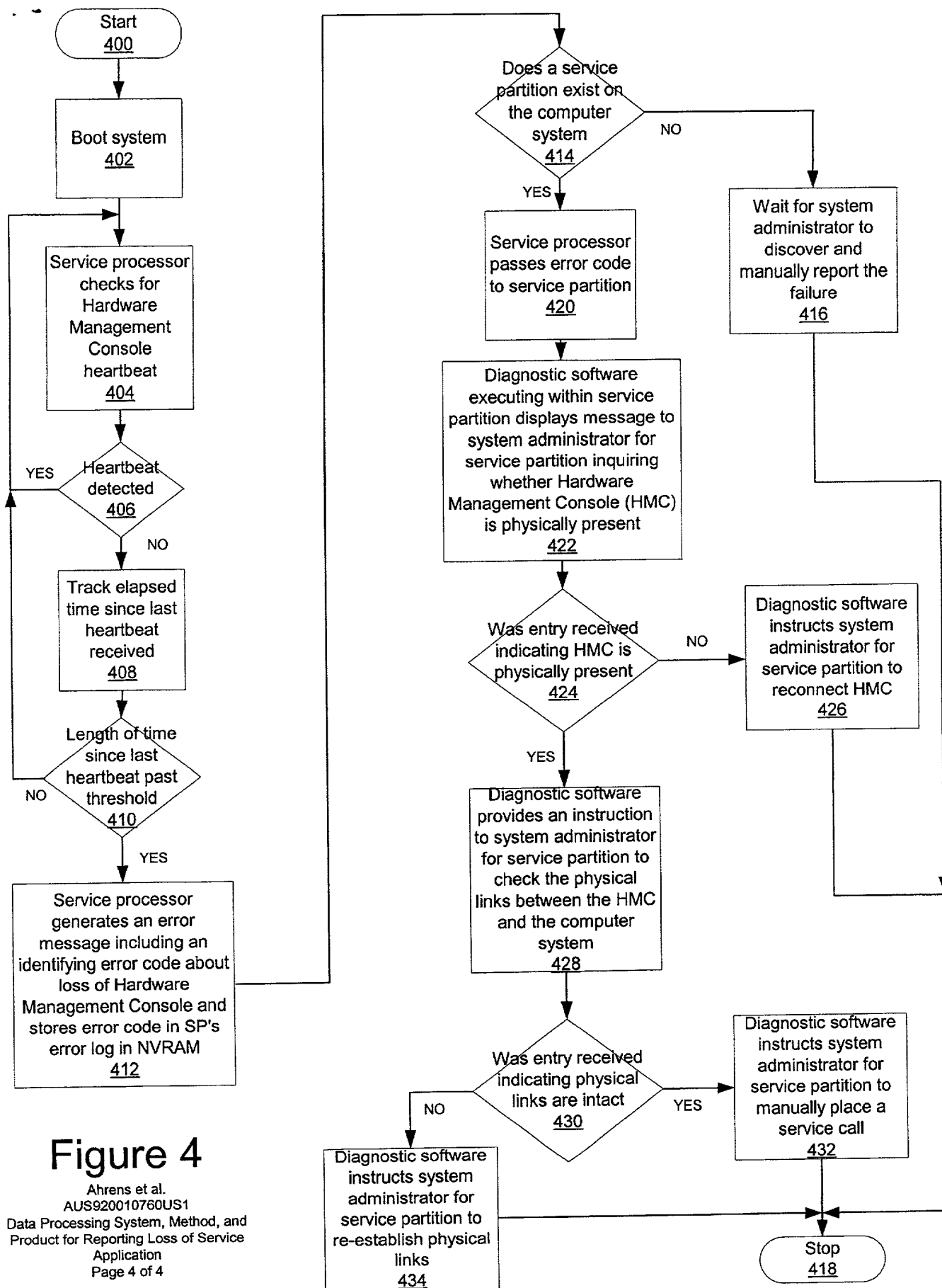


Figure 4

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